Claim Amendments

Please make the following amendments to the claims:

1   1.   (CURRENTLY AMENDED)   A processor-based system, comprising:

2   a performance control apparatus, comprising a <u>first</u> selector, the <u>first</u>

3   selector being adjustable, between a minimum setting and a maximum setting,

4   <u>to modify one or more performance criteria of the processor-based system, the</u>

5   <u>performance criteria comprising a processor clock rate, a fan speed, and disk</u>

6   <u>usage;</u> and

7   a performance control application program with a graphical user interface,

8   the graphical user interface comprising at least one application program selector

9   associated with an application program loaded in the processor-based system,

10   wherein the application program selector is adjustable between a second

11   minimum setting and a second maximum setting;

12   wherein the at least one application program selector enables a user to modify

13   <u>the</u> one or more performance criteria during operation of the application program

14   and the <u>first</u> selector enables the user to modify the one or more performance

15   criteria during operation of the processor-based system.

1   2.   (CURRENTLY AMENDED)   The processor-based system of claim 1, <u>further</u>

2   <u>comprising a second selector,</u> wherein the [one or more performance criteria of

3   the processor-based system include] processor clock rate <u>is controlled by the first</u>

4   <u>selector</u> and <u>the</u> fan speed <u>is controlled by the second selector, the first selector</u>

5   <u>and the second selector being independently controllable.</u>

1 3. (CURRENTLY AMENDED) The processor-based system of claim 2, the
2 first selector of the performance control apparatus further comprising first and
3 second labels disposed at opposing ends of the first selector, the first label
4 indicating the minimum setting and the second label indicating the maximum
5 setting.

1 4. (ORIGINAL) The processor-based system of claim 3, the performance
2 control apparatus further comprising a display, the display having first and
3 second indicators, wherein the first indicator conveys a processor temperature
4 and the second indicator conveys a relative performance value of the processor-
5 based system.

1 5. (CURRENTLY AMENDED) The processor-based system of claim 4,
2 wherein the processor clock rate may exceed an optimum clock rate when the
3 first selector is adjusted beyond a predetermined setting, wherein the
4 predetermined setting is not the maximum setting.

1 6. (CURRENTLY AMENDED) The processor-based system of claim 5,
2 wherein the first selector further comprises a plurality of light-emitting diodes,
3 wherein one or more of the plurality of diodes sequentially lights up when the
4 first selector is adjusted.

1 7. (CURRENTLY AMENDED) The processor-based system of claim 6,
2 wherein one or more of the plurality of light-emitting diodes change color state
3 when the first selector is adjusted beyond the predetermined setting.

1　8.　(CURRENTLY AMENDED)　The processor-based system of claim 1,
2　wherein [the one or more performance criteria of the processor-based system
3　include a processor clock rate and] the application program selector enables the
4　user to adjust and set the processor clock rate during execution of the
5　application program.

1　9.　(CURRENTLY AMENDED)　The processor-based system of claim 8,
2　wherein [the one or more performance criteria of the processor-based system
3　include a fan speed and] the performance control application program further
4　comprises a second application program selector for enabling the user to adjust
5　the fan speed during execution of the application program.

1　10.　(ORIGINAL)　The processor-based system of claim 1, further comprising a
2　performance control icon, accessible from within the application program,
3　wherein the performance control icon enables the user to modify one or more
4　performance criteria from within the application program.

1　11.　(CURRENTLY AMENDED)　A performance control apparatus, comprising:
2　　　　[a selector] a plurality of selectors for designating one of several settings
3　in a processor-based system, wherein each setting [is associated with] modifies
4　one or more performance-related criteria of the processor-based system, the
5　performance-related criteria comprising a processor clock rate, a fan speed, and
6　a disk drive usage of the processor-based system, wherein each performance-
7　related criterion is associated with a separate selector of the plurality of
8　selectors; and

9   a display comprising an indicator, wherein the indicator visually conveys a

10   relative performance value for the processor-based system.

1   12.   (ORIGINAL) The apparatus of claim 11, further comprising a first label

2   and a second label, the first and second labels being disposed adjacent to the

3   selector, wherein the first label designates a minimum setting of the selector and

4   the second label designates a maximum setting of the selector.

1   13.   (CURRENTLY AMENDED) The apparatus of claim 12, further comprising

2   a plurality of light-emitting diodes, the plurality of light-emitting diodes being

3   disposed adjacent to the selector, [wherin] wherein one or more of the plurality

4   of light-emitting diodes changes to a first color when the selector is not at the

5   minimum setting.

1   14.   (CURRENTLY AMENDED) The apparatus of claim 13, wherein the

2   [performance-related criteria comprise a processor clock rate] plurality of

3   selectors comprises a first selector for controlling the processor clock rate and a

4   second selector for controlling the fan speed, wherein the first and second

5   selectors are independently controllable.

1   15.   (ORIGINAL) The apparatus of claim 14, wherein the processor clock rate

2   may exceed an optimum clock rate.

1   16.   (ORIGINAL) The apparatus of claim 15, wherein one or more of the

2   plurality of light-emitting diodes change to a second color when the processor

3   clock rate exceeds the optimum clock rate.

1   17.   (CURRENTLY AMENDED) The apparatus of claim 12, wherein the

2   [performance-related criteria comprise a processor clock rate and a fan speed]

3   plurality of selectors comprises a first selector for controlling both the processor

4   clock rate and the fan speed, wherein adjustment of the first selector

5   simultaneously controls the fan speed and the processor clock rate.

1   18.   (ORIGINAL) The apparatus of claim 14, wherein the display further

2   comprises a second indicator, wherein the second indicator visually conveys a

3   processor temperature.

1   19.   (CURRENTLY AMENDED) The apparatus of claim [17] 14, [wherein the

2   performance-related criteria comprise disk drive usage], the plurality of selectors

3   further comprising a third selector, the third selector being adjustable to modify

4   the disk drive usage of the processor-based system by an application program,

5   wherein the third selector adjusts between the application program being

6   executed from the disk drive and being executed from a volatile memory.

1   20.   (CANCELLED)   The apparatus of claim 10, wherein the performance-

2   related criteria comprise processor speed and fan speed, the apparatus further

3   comprising a second selector, wherein the selector controls the processor speed

4   and the second selector controls the fan speed.

1   21.   (CURRENTLY AMENDED) A performance control application program, to

2   be run on a processor-based system, the performance control application

3   program being viewable from a graphical user interface, the graphical user

4   interface comprising:

5   a list of one or more software programs loaded into the processor-based

6   system; and

7   a selector for altering a [first performance-based characteristic] a

8   processing speed of the processor-based system;

9   wherein the [first performance-based characteristic] processing speed is altered

10   while one software program of the one or more software programs is running on

11   the processor-based system, but is not altered when the one software program

12   is not running.

1   22.   (CURRENTLY AMENDED)   The performance control application program

2   of claim 21, a portion of the one or more software programs being collected as a

3   group, wherein the [first performance-based characteristic] processing speed is

4   altered when any software program in the group is running.

1   23.   (CURRENTLY AMENDED)   The performance control application program

2   of claim 22, the graphical user interface further comprising a second selector for

3   altering a [second performance-based characteristic] system noise characteristic,

4   wherein the first selector is independent of the second selector.

1   24.   (CURRENTLY AMENDED)   A performance control application program, to

2   be run on a processor-based system, the performance control application

3   program being viewable from a graphical user interface, the graphical user

4   interface comprising:

5   a file type grouping, the file type grouping specifying a plurality of file

6   extensions; and

7   a configuration profile associated with the file type grouping, wherein the

8   configuration profile specifies [a set of performance criteria for] adjusting the

9   speed of one or more fans operating within the processor-based system;

10   wherein the processor-based system automatically sets the configuration profile

11   when a file having one of the plurality of file extensions is run.

1   25.   (CURRENTLY AMENDED)   The performance control application program

2   of claim 24, further comprising:

3   a second file type grouping, the file type grouping specifying a second

4   plurality of file extensions, the second plurality of file extensions being distinct

5   from the first plurality of file extensions; and

6   a second configuration profile associated with the second file type

7   grouping, wherein the second configuration profile specifies [a second set of

8   performance criteria for] adjusting a processor clock rate of the processor-based

9   system[;

10   wherein the second set of performance criteria is different than the set of

11   performance criteria].

1   26.   (CANCELLED)   The performance control application program of claim

2   25, wherein the set of performance criteria comprise adjusting the speed of one

3   or more fans operating within the processor-based system.

1   27.   (CANCELLED)   The performance control application program of claim

2   25, wherein the second set of performance criteria comprise adjusting a

3   processor clock rate.